

## PROGRAMMABLE LIGHT-ACTIVATED TIMER

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### FEATURES:

- Input interface to Photo Resistor
- Programmable Duration Selection
- Shunt regulator
- 50Hz / 60Hz time base selection
- Relay Driver output
- 6.0V ± 0.75V operating voltage range (V<sub>DD</sub> - V<sub>SS</sub>)
- **LS7217** (DIP), **LS7217-S** (SOIC) - See Figure 1 -

### APPLICATIONS

Lighting control for low voltage landscape lighting, etc.

### DESCRIPTION

The **LS7217** is a Programmable Timer IC designed to turn on a relay at night and turn off the relay at dawn or after a selectable number of hours. Figure 2 shows a typical application schematic. The circuit is powered from a step-down transformer and the output drives low-voltage AC loads such as incandescent light bulbs.

### PIN DESCRIPTION:

The following describes the operation of the inputs and outputs of the IC.

#### V<sub>DD</sub> (Pin 2)

V<sub>DD</sub> is the supply voltage positive terminal. It is regulated internally in the IC. The **internal voltage regulator produces 6.0V ± 0.75V**. A series resistor is required to limit current into the regulator from 5mA to 10mA over the full transformer operating voltage range.

#### V<sub>SS</sub> (Pin 6)

V<sub>SS</sub> is the supply voltage negative terminal.

#### 50Hz / 60 Hz SELECT Input (Pin 8)

A **high** at this at this input selects the correct timing for 50Hz operation. **Floating** this input selects the correct timing for 60Hz operation. A **low** at this input places the **LS7217** into **Test Mode** where the timeouts are accelerated by a factor of 60.

#### 50Hz / 60 Hz Input (Pin 7)

This input is the clock source for all timing functions. This input accepts a 9VAC to 16VAC input voltage. An external dropping resistor of 220k Ohms is needed to limit current. This input has a Schmitt trigger to ensure a clean internal clock waveform.

### PIN ASSIGNMENT TOP VIEW

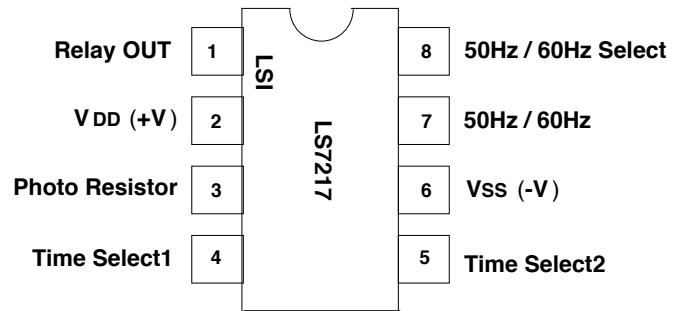


FIGURE 1

### TIME SELECT Inputs TS1, TS2 (Pins 4, 5)

The two Select inputs determine the time duration that the Relay output stays on after the photocell goes low. Both inputs have internal pulldown transistors so that float is logic zero and connection to V<sub>DD</sub> is logic 1. The Time Select Table is as follows:

Time Select1	Time Select2	Time Duration
0	0	4 Hours
0	1	6 Hours
1	0	8 Hours
1	1	Dusk -to-Dawn

Dusk to Dawn duration is determined solely by the photocell; i.e., the Relay output is on whenever the photocell recognizes an ambient dark condition.

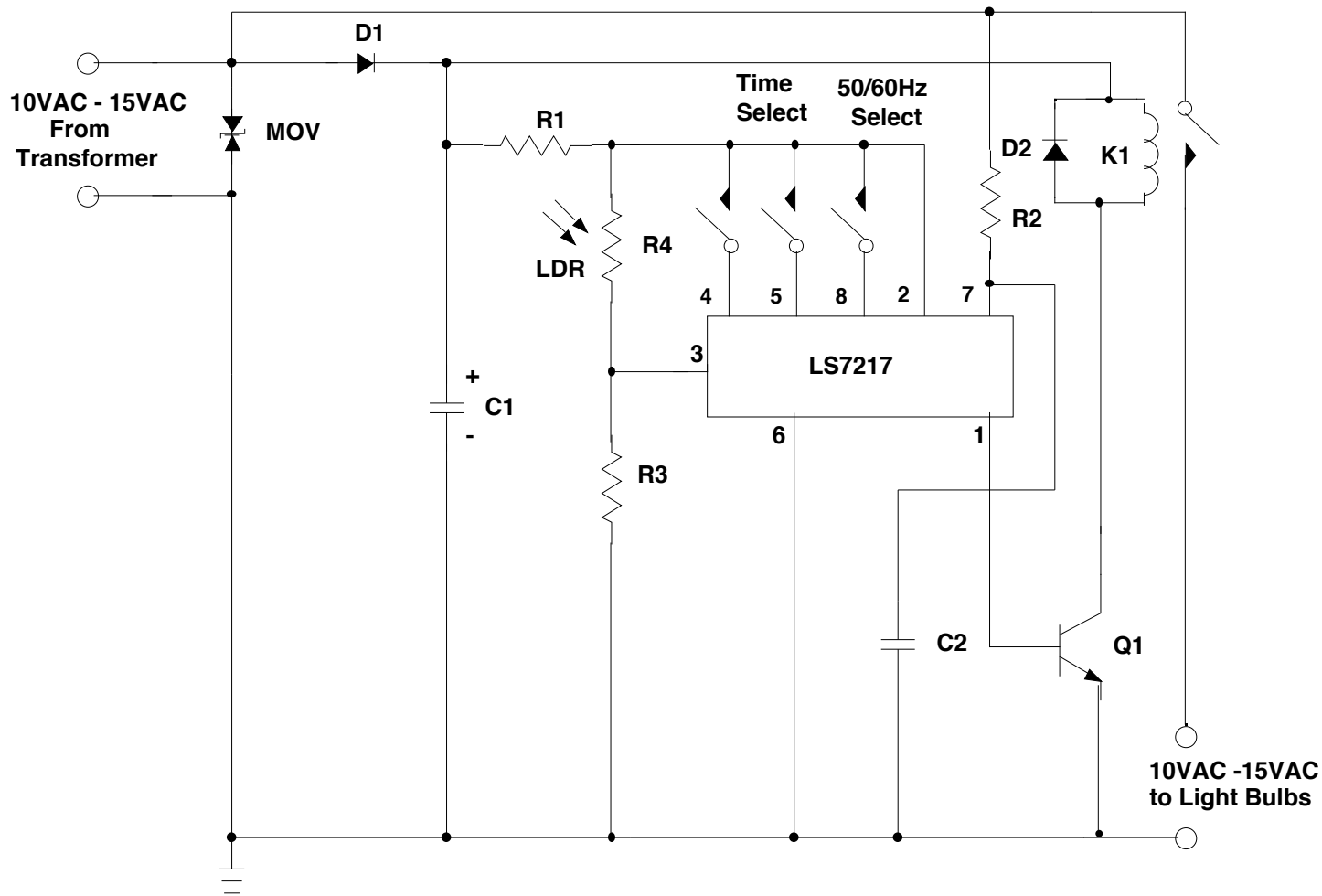
### PHOTO RESISTOR Input (Pin 3)

The photo resistor input has hysteresis for a positive trip point. The input will work with a photo resistor connected between the input and V<sub>DD</sub>. The photo resistor provides a low impedance in the presence of ambient light and a high impedance in the presence of ambient darkness.

The IC is configured so that detection of a **light condition** must remain for 6.0 seconds (± 0.5 seconds) continuously in order to be recognized as a **valid light condition**. A **dark condition** must remain for 1.0 seconds (± 0.25 seconds) continuously in order to be recognized as a **valid dark condition**.

### RELAY Output (Pin 1)

This output is configured to drive the base of an external NPN transistor (see Figure 2).



**FIGURE 2. TYPICAL LANDSCAPE LIGHTING APPLICATION SCHEMATIC**

D1 = 1N4004

D2 = 1N4004

C1 = 220uF, 25V

C2 = 470pF, 10V

Q1 = 2N3904 (Typical)

R1 = 1k

R2 = 220k

R3 = 25k

R4 = **Silonex** NSL-19M51 (Typical)

**Note:** All resistors, 1/4W

**ABSOLUTE MAXIMUM RATINGS:** (All voltages referenced to Vss)

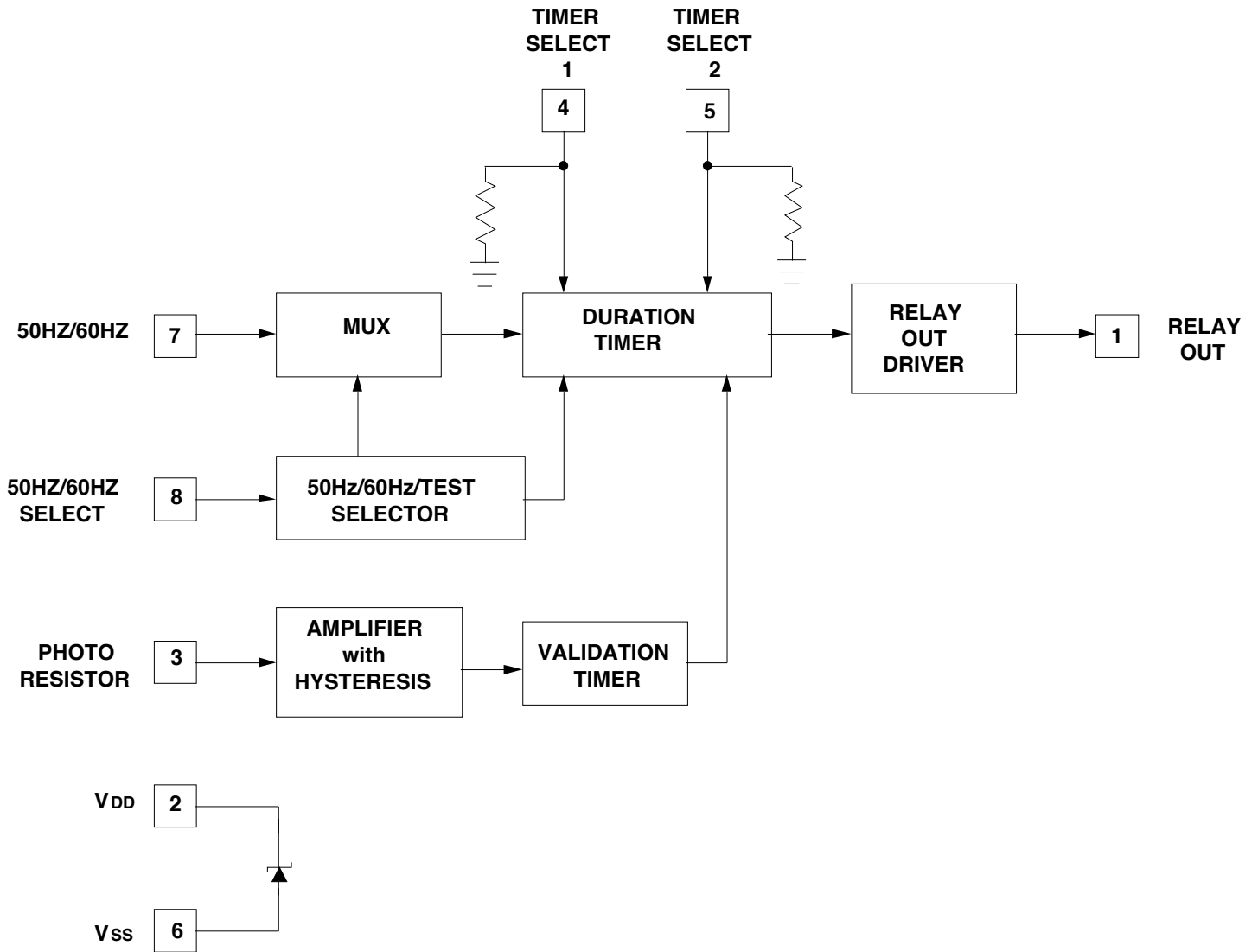
	<b>SYMBOL</b>	<b>VALUE</b>	<b>UNIT</b>
DC Supply Voltage	VDD	+8	V
Voltage (Any Pin)	VIN	VSS - 0.3 to VDD + 0.3	V
Operating Temperature	TA	-20 to +85	°C
Storage Temperature	TSTG	-40 to +150	°C

**DC ELECTRICAL CHARACTERISTICS:**

(TA = 25°C, VDD = 6.0V unless otherwise specified.)

<b>PARAMETER</b>	<b>SYMBOL</b>	<b>MIN</b>	<b>TYP</b>	<b>MAX</b>	<b>UNIT</b>
TS1, TS2 Low	TLO	-	-	1.4	V
TS1, TS2 High	THI	3.0	-	-	V
50/60 Hz Low	VHZL	-	-	2.9	V
50/60 Hz High	VHZH	4.4	-	-	V
<b>Photo Resistor</b>					
Inhibit Threshold	VIT	3.5	-	-	V
Enable Threshold	VET	-	-	2.9	V
<b>Input Current</b>					
(All inputs high)					
50Hz/60Hz Select ] TS1, TS2 ]	I <sub>IH</sub>	-	24	-	mA
<b>Output Current</b>					
Sourcing, Vo = 0.7V	I <sub>OH</sub>	4.0	-	-	mA
Sinking, Vo = 0.4V	I <sub>OL</sub>	-5.0	-	-	mA

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**FIGURE 3. LS7217 BLOCK DIAGRAM**